

FIG. 1

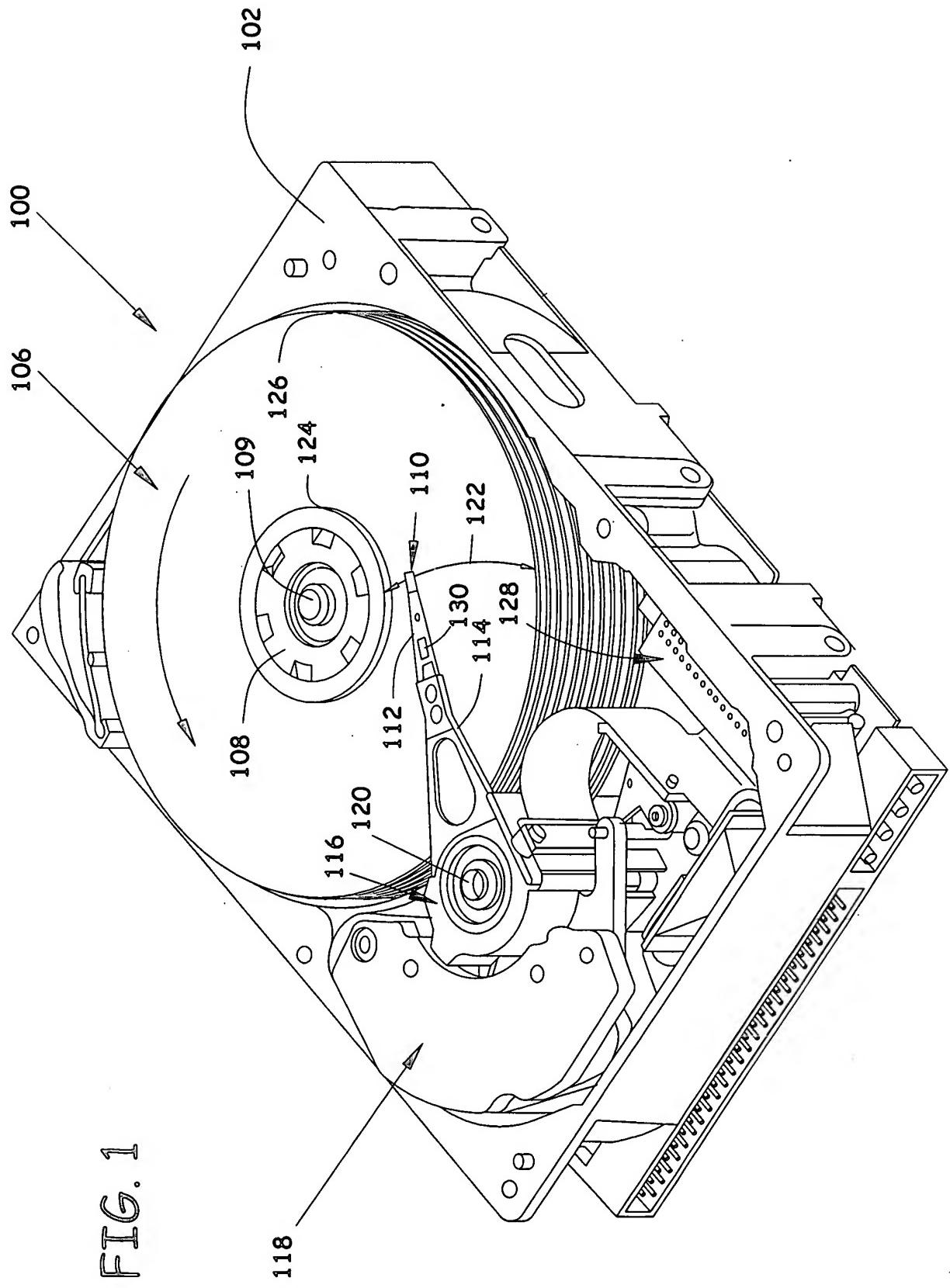
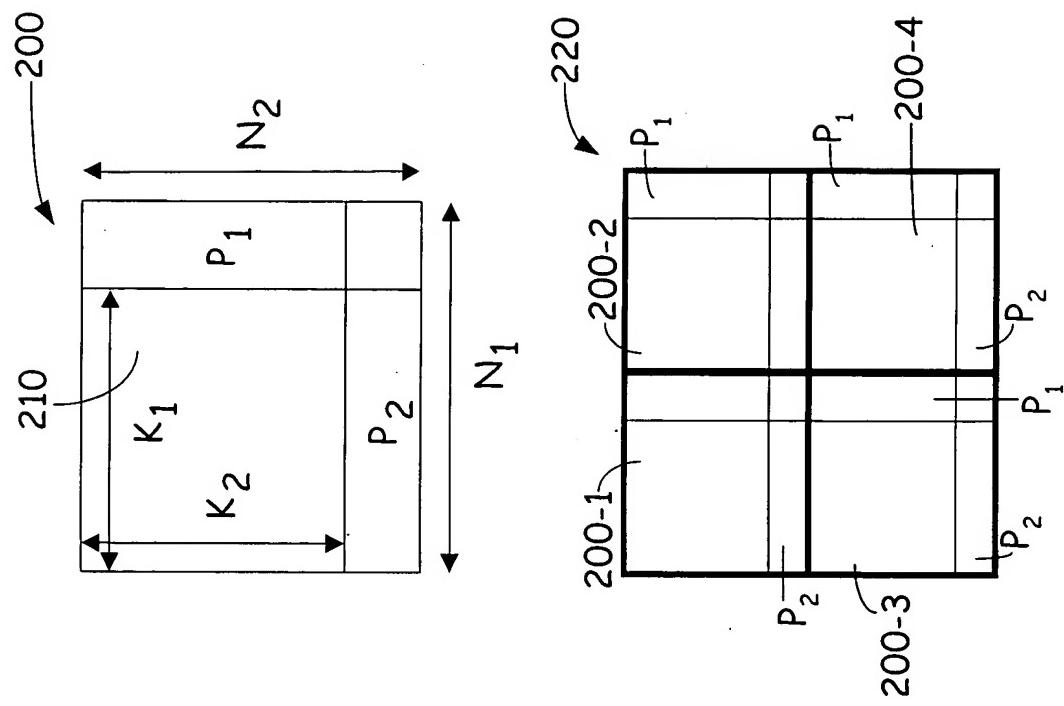


FIG. 2



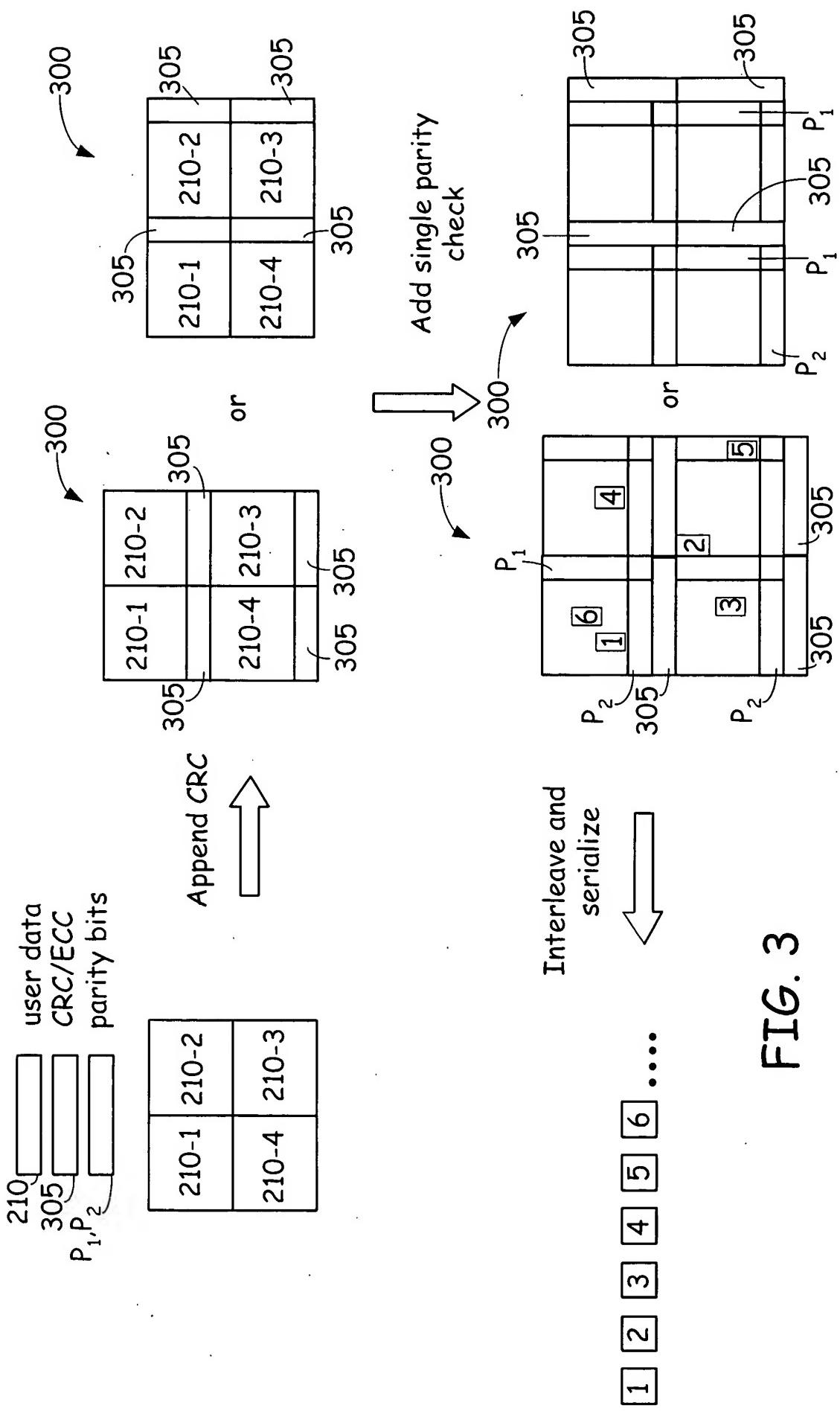


FIG. 3

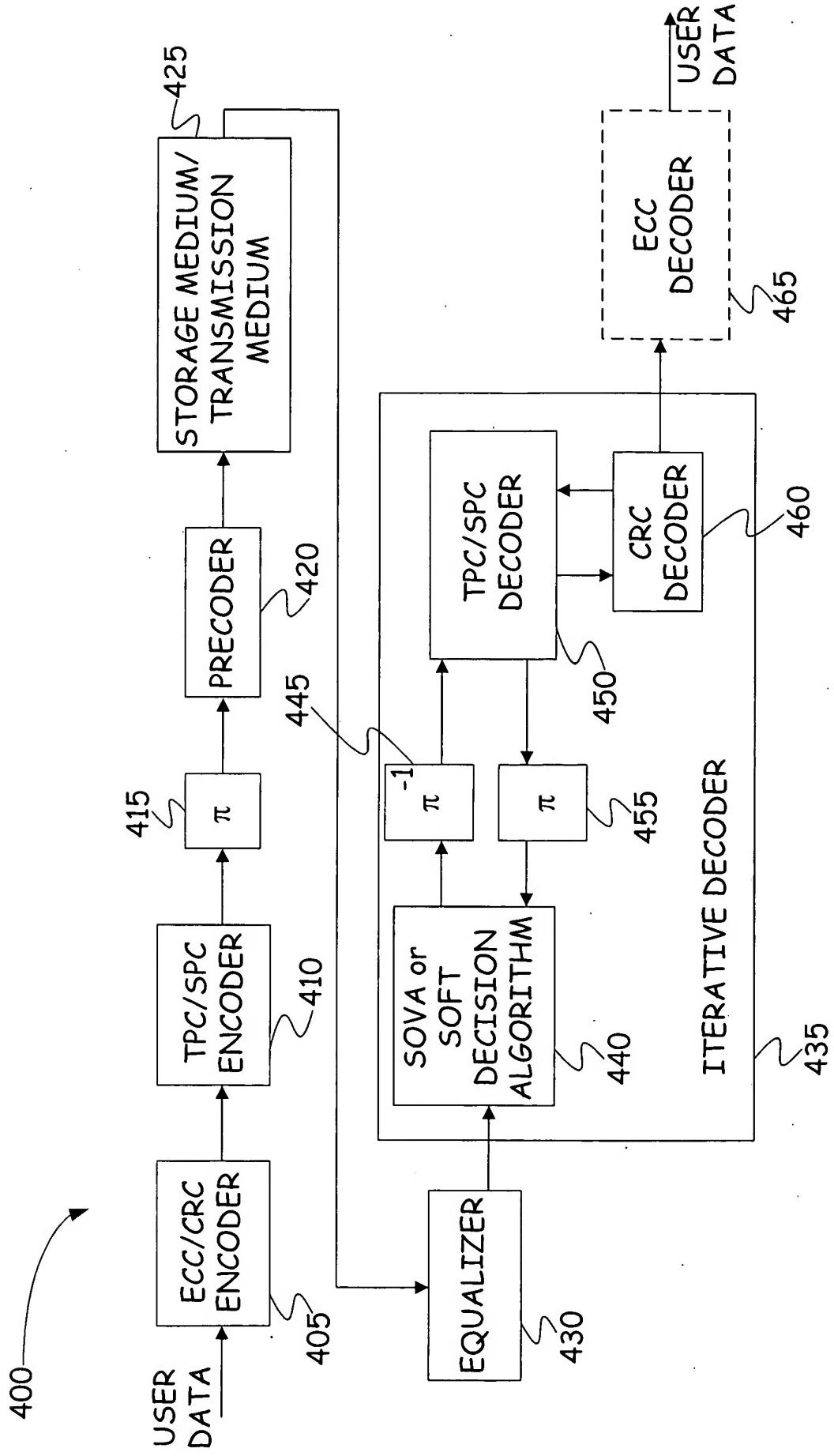
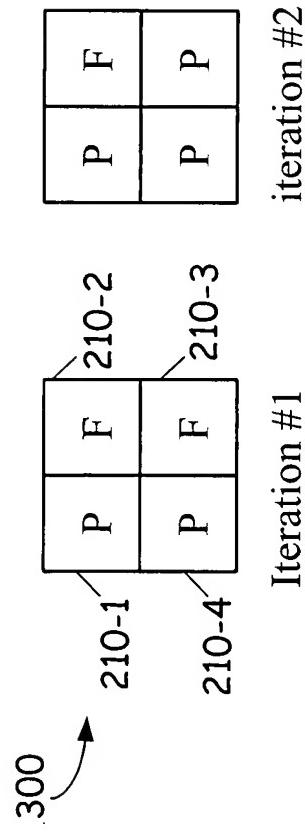


FIG. 4

- Perform posterior message computation
- Not perform posterior message computation

No sub-block CRC check:



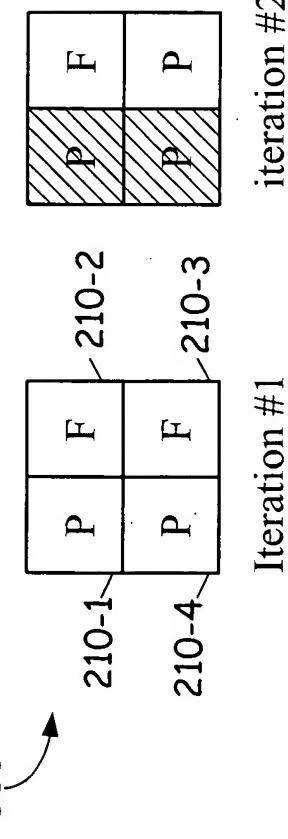
P: Pass

F: Fail

P	P
P	P

iteration #3

With CRC sub-block check:



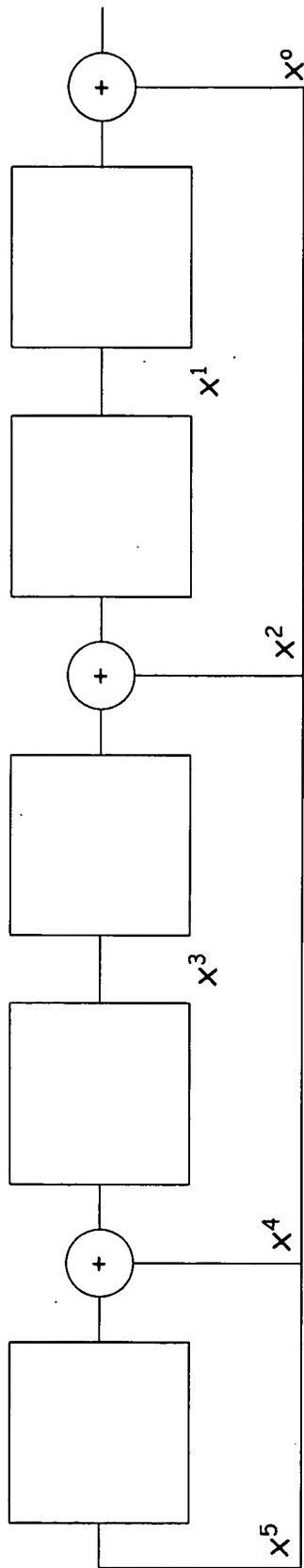
P	P
P	P

iteration #3

FIG. 5

Input bits when creating CRC 101000110100000
Input bits when creating CRC 101000110101110

460



$$P(x) = x^5 + x^4 + x^2 + 1$$

FIG. 6

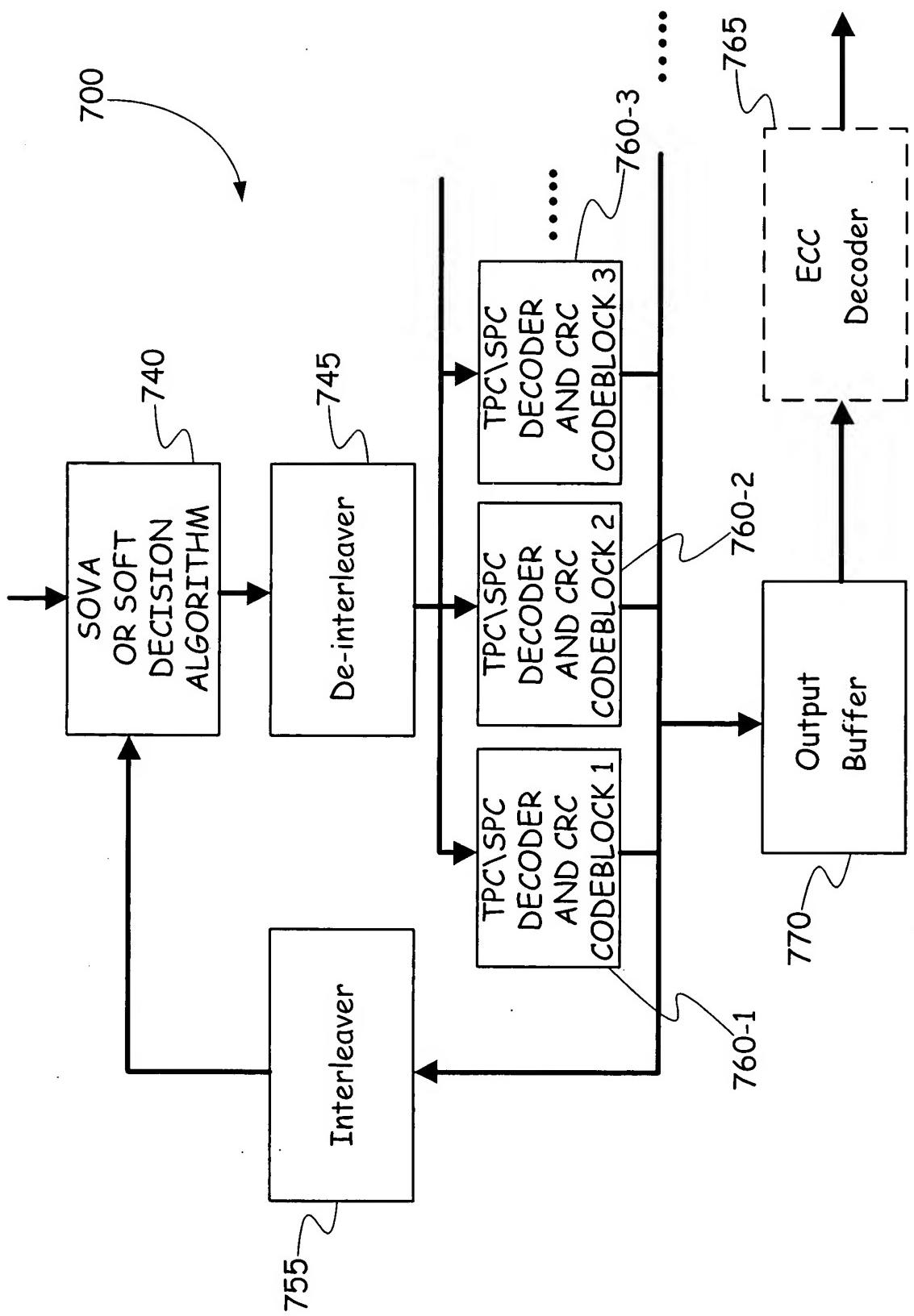


FIG. 7

FIG. 8-1

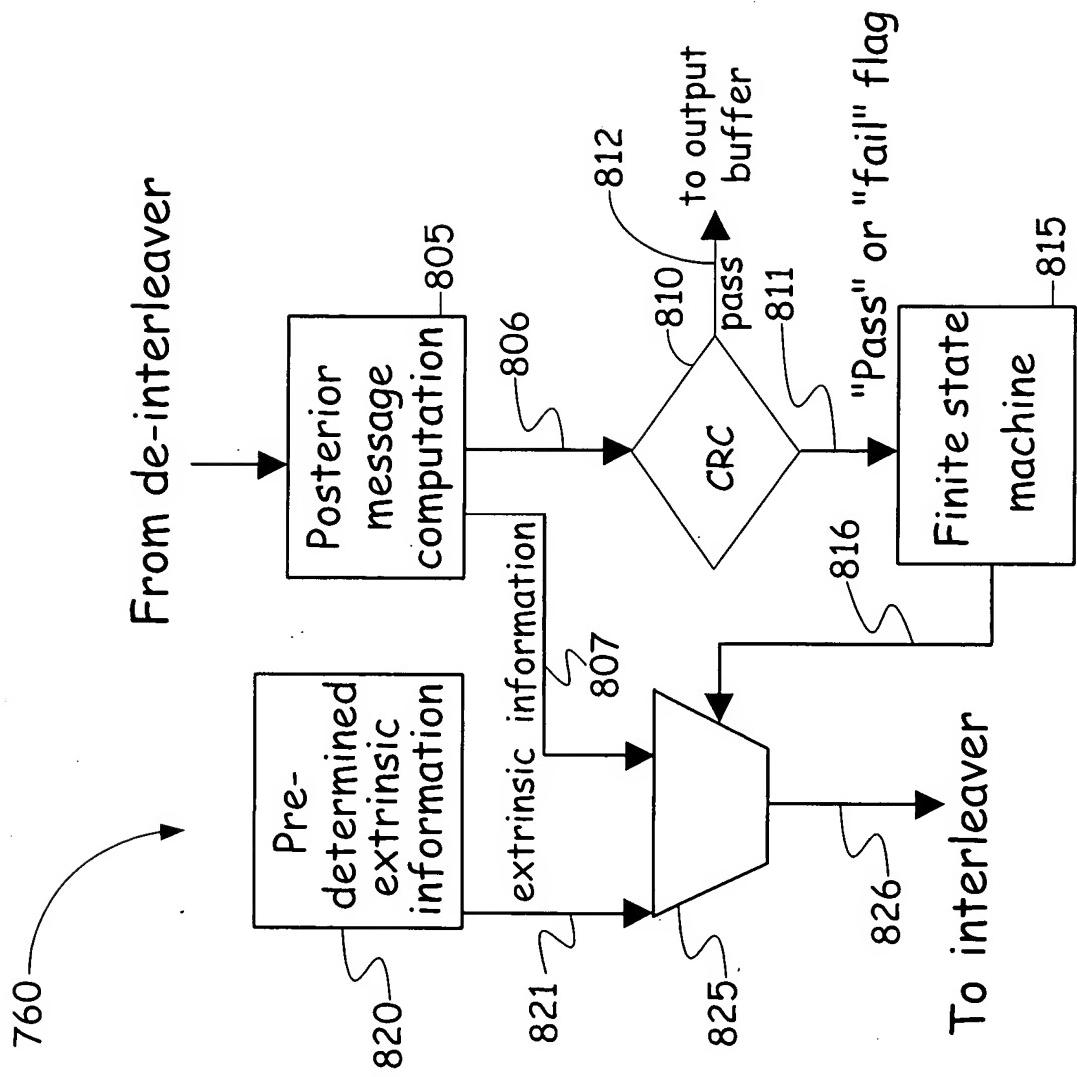
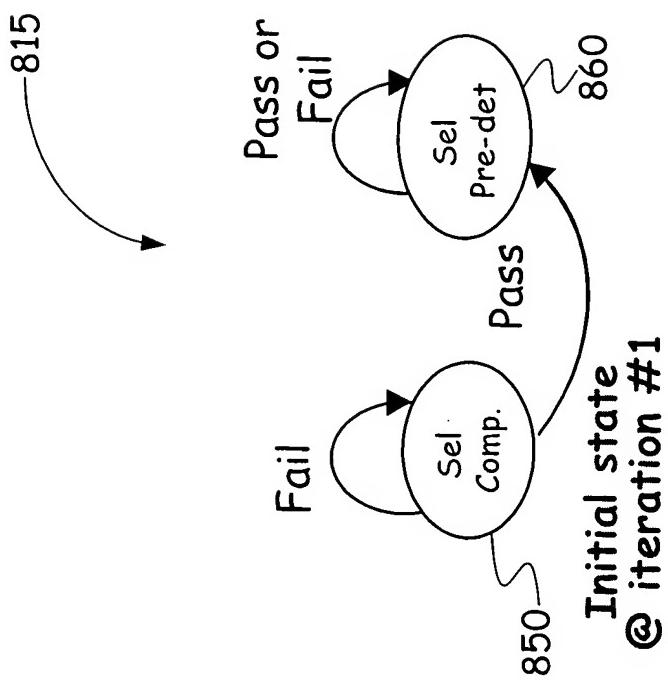
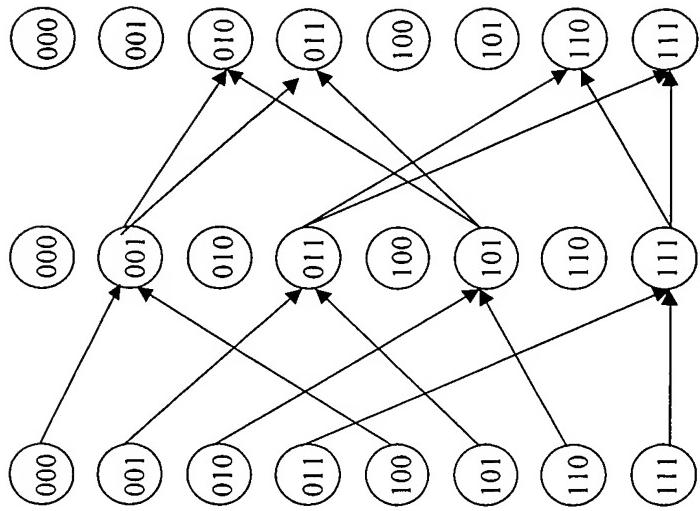


FIG. 8-2

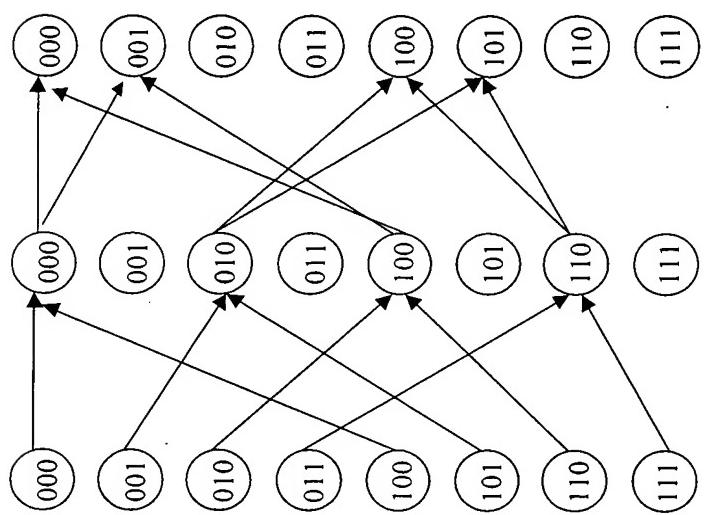
Finite state
machine



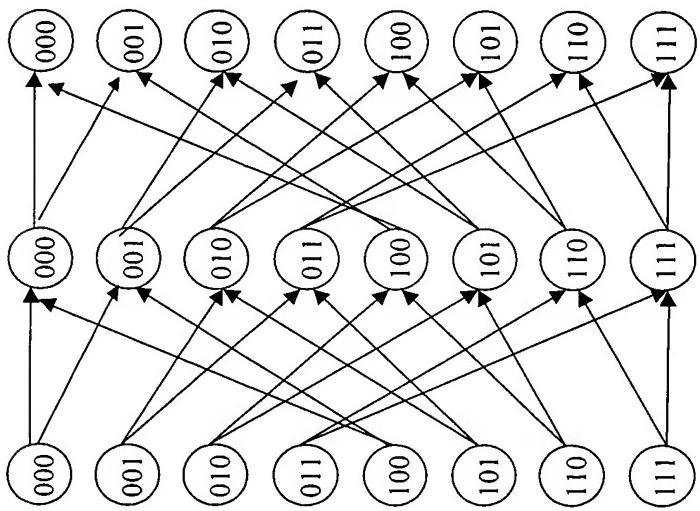
Definite "1"



Definite "0"



Normal trellis



Trellis with the second column to have
a definite value

FIG. 9